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TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT (Under 37 CFR 1.97(b) or 1.97(c))			Docket No. SKI.008C2	
In Re Application Of: Shunji Hayashi				
Serial No. 10/712,100	Filing Date November 14, 2003	Examiner Unknown	Group Art Unit 2825	
Title: METHOD AND SYSTEM FOR MANAGING SEMICONDUCTOR MANUFACTURING EQUIPMENT				
<p>Address to: Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450</p> <p>37 CFR 1.97(b)</p> <p>1. <input checked="" type="checkbox"/> The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.</p> <p>37 CFR 1.97(c)</p> <p>2. <input type="checkbox"/> The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:</p> <p><input type="checkbox"/> the statement specified in 37 CFR 1.97(e);</p> <p>OR</p> <p><input type="checkbox"/> the fee set forth in 37 CFR 1.17(p).</p> <p>Note: The enclosed references were cited in copending U.S. Application Serial No. 10/125,403, and thus may be material to the prosecution of the present application.</p>				

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MAY 18 2004

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Examiner
Unknown

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2825

METHOD AND SYSTEM FOR MANAGING SEMICONDUCTOR MANUFACTURING

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Dated: May 18, 2004

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